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Computer Methods for Circuit Analysis and Design Second Edition Jiri Vlach Department of Electrical Engineering University of Waterloo Waterloo, Ontario, Canada

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Basic Circuit Analysis Method (KVL and KCL method) Cite as: Anant Agarwal and Jeffrey Lang, course materials for 6.002 Circuits and Electronics, Spring 2007.

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Printed Circuit Board Recycling Methods - US EPA

Fei Yuan, Ajoy Opal, 'Computer Methods for Analysis of Mixed-Mode Switching Circuits' 2004 | pages: 385 | ISBN: 1402079222 | PDF | 11,0 mb -N

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